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**FACSIMILE COVER SHEET**

To: Examiner Monica Lewis  Technology Center 2800	Total Pages Sent: 2  (including cover sheet)
Facsimile Number: (571) 273-8300	Transmission Date: December 21, 2005

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Thomas, *et al.* Docket No.: 2003 P 54322 US  
 Serial No: 10/790,907 Art Unit: 2822  
 Date Filed: March 2, 2004  
 Title: Integrated Circuit with Re-Route Layer and Stacked Die Assembly

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- Certification of Facsimile Transmission (1 page)
- Election (1 page)

Respectfully submitted,

  
 Kristin R. Hayes  
 Legal Assistant

**Confirmation Respectfully Requested**

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Applicant: Thomas, *et al.* Docket No.: 2003 P 54322 US  
Serial No.: 10/790,907 Art Unit: 2822  
Filed: March 2, 2004 Examiner: Lewis, Monica  
For: Integrated Circuit with Re-Route Layer and Stacked Die Assembly

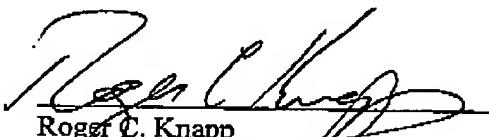
Mail Stop Amendment  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

ELECTION

Dear Sir:

In response to the restriction requirement set forth in the Office Action mailed on November 30, 2005, Applicants hereby elect without traverse Group II, Claims 53-91, for further prosecution in the above patent application.

Respectfully submitted,



Roger C. Knapp  
Attorney for Applicants  
Reg. No. 46,836

December 21, 2005  
Date

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